PTO/SB/08A(08-03)
Approved for use through 07/31/2006. CMB 0651-0031
Petert & Trademert Office U.B. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO	nder the Paperwork Reduction Act of 1995, no persons are r Complete if Known	required to respond to a collection of information unless it contains a veid OMB control number
INFORMATION DISCLOSURE	Application Number	09/687,777
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Filing Date	October 13, 2000
70,167	First Named Inventor	Patel, Mukesh
Company of the compan	Group Art Unit	2122
MAY 0 9 2005 6	Examiner Name	Das, Chameli
Sheet 1 of 1	Attorney Docket No: 0	00006.P001X
Tought		<del></del>

	US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines,Where Relevant Passages or Relevant Figures Appear			
0.0	US-6,003,038	12/14/1999	Chen, Tao S., et al.				
O	US-6,065,108	05/16/2000	Tremblay, Mark , et al.				
S	US-6,275,903	08/14/2001	Koppala, Sailendra , et al.				
CO	US-6,292,883	09/18/2001	Augustijn, Alexander , et al.				
00	US-6,317,872	11/13/2001	Gee, John K., et al.				
ch	US-6,321,323	11/20/2001	Nugroho, Sofyan I., et al.				
S	US-6,330,659	11/12/2001	Poff, Thomas C., et al.				
(1)	US-6,374,286	04/16/2002	Gee, John K., et al.				
co	US-6,532,531	03/11/2003	O' Conner, Michael , et al.				
CD	US-6,606,743	08/12/2003	Raz, Yair, et al.				

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T²

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No 1	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
CD		ROSE, A C., "Hardware Java Accelerator for the ARM 7", 4th Year Undergraduate Project in Group D, (1996/97),1-49, Appendix	
Co		STEINBUSCH, OTTO, "Designing Hardware to Interpret Virtual Machine Instructions", Dept. of Electrical Engineering, Eindhoven University of Technology, Masters Degree Thesis, February 1998, 59	

/1						-
EXAMINER CK	aml-c	~ C-	DATE CONSIDERED	J	27/0	1